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(71) Applicant: **HITACHI, LTD.**
Chiyoda-ku, Tokyo 101 (JP)

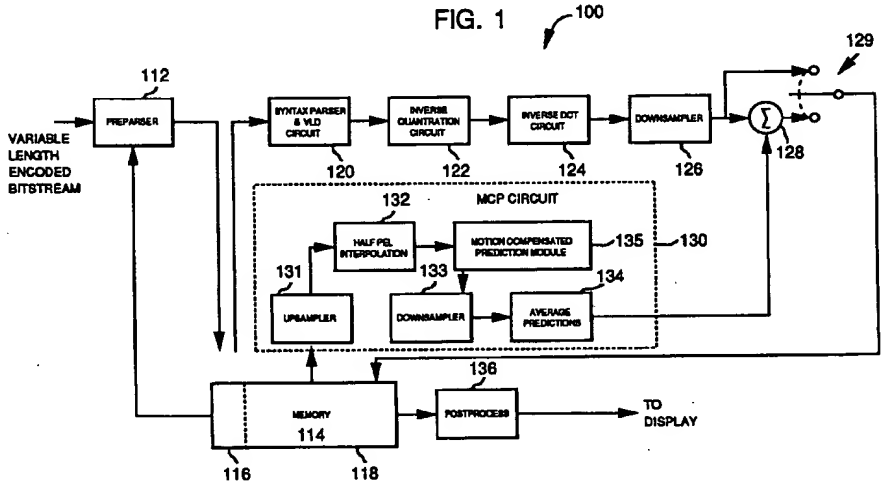
(72) Inventors:
- **Boyce, Jill MacDonald**
Manalapan, New Jersey 07726 (US)
- **Pearlstein, Larry**
Newton, PA 18940 (US)

(74) Representative: **Altenburg, Udo, Dipl.-Phys. et al**
Patent- und Rechtsanwälte,
Bardehle . Pagenberg . Dost . Altenburg . Frohwitter . Geissler & Partner,
Galileiplatz 1
D-81679 München (DE)

(54) Digital video decoder for decoding digital high definition and/or digital standard definition television signals

(57) Methods and apparatus for reducing the complexity of decoder circuitry and video decoder memory requirements are disclosed. The discribed video decoders are capable of decoding HDTV and/or SDTV pictures. The described video decoder may be used as part of a picture-in-picture decoder circuit for providing picture-in-picture capability without providing multiple full resolution video decoders. The reduction in decoder circuit complexity is achieved through the use of a plurality of data reduction techniques including the use of a preparser, downsampling, and truncating pixel values.

FIG. 1



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